## **General Purpose Transistors**

#### **PNP Silicon**

This transistor is designed for general purpose amplifier applications. It is housed in the SOT-416/SC-75 package which is designed for low power surface mount applications.

#### **Features**

• Pb-Free Package is Available

#### **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	-40	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	-40	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	Vdc
Collector Current – Continuous	Ic	-200	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) @T <sub>A</sub> = 25°C Derated above 25°C	P <sub>D</sub>	200 1.6	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	600	°C/W
Total Device Dissipation, FR-4 Board (Note 2) @T <sub>A</sub> = 25°C Derated above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	400	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

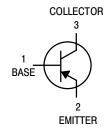
- 1. FR-4 @ Minimum Pad
- 2. FR-4 @ 1.0 × 1.0 Inch Pad



#### ON Semiconductor®

http://onsemi.com

# GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT





CASE 463 SOT-416/SC-75 STYLE 1

#### MARKING DIAGRAM



2A = Device Code M = Date Code\*

= Pb–Free Package

(Note: Microdot may be in either location)
\*Date Code orientation may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

	Device	Package	Shipping <sub>†</sub>
ſ	MMBT3906TT1	SOT-416	3000 / Tape & Reel
ľ	MMBT3906TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure. BRD8011/D.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

	Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERIS	TICS	•		_	
Collector – Emitter Bre (I <sub>C</sub> = –1.0 mAdc, I	eakdown Voltage (Note 3) <sub>B</sub> = 0)	V <sub>(BR)</sub> CEO	-40	_	Vdc
Collector – Base Brea (I <sub>C</sub> = –10 μAdc, I <sub>E</sub>		V <sub>(BR)</sub> CBO	-40	_	Vdc
Emitter – Base Breako (I <sub>E</sub> = –10 μAdc, I <sub>C</sub>		V <sub>(BR)EBO</sub>	-5.0	_	Vdc
Base Cutoff Current (V <sub>CE</sub> = -30 Vdc, V	/ <sub>EB</sub> = -3.0 Vdc)	I <sub>BL</sub>	_	-50	nAdc
Collector Cutoff Curre (V <sub>CE</sub> = -30 Vdc, V		I <sub>CEX</sub>	_	-50	nAdc
ON CHARACTERIST	ICS (Note 3)				
DC Current Gain	$V_{CE} = -1.0 \text{ Vdc}$ $V_{CE} = -1.0 \text{ Vdc}$ $V_{CE} = -1.0 \text{ Vdc}$	h <sub>FE</sub>	60 80 100 60 30	- 300 - -	_
Collector – Emitter Sa (I <sub>C</sub> = -10 mAdc, I <sub>E</sub> (I <sub>C</sub> = -50 mAdc, I <sub>E</sub>	$_{3} = -1.0 \text{ mAdc}$	V <sub>CE(sat)</sub>	_ _	-0.25 -0.4	Vdc
Base – Emitter Satura $(I_C = -10 \text{ mAdc}, I_E  (I_C = -50 \text{ mAdc}, I_E)$	$_{3} = -1.0 \text{ mAdc}$	V <sub>BE(sat)</sub>	-0.65 -	-0.85 -0.95	Vdc
SMALL-SIGNAL CH	ARACTERISTICS				
Current – Gain – Band (I <sub>C</sub> = –10 mAdc, V	dwidth Product $f'_{CE} = -20 \text{ Vdc, } f = 100 \text{ MHz}$	f <sub>⊤</sub>	250	-	MHz
Output Capacitance (V <sub>CB</sub> = -5.0 Vdc, I	<sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	_	4.5	pF
Input Capacitance1 (V <sub>EB</sub> = -0.5 Vdc, I	<sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ibo</sub>	_	10.0	pF
Input Impedance (V <sub>CE</sub> = -10 Vdc, I <sub>0</sub>	<sub>C</sub> = −1.0 mAdc, f = 1.0 kHz)	h <sub>ie</sub>	2.0	12	kΩ
Voltage Feedback Ra (V <sub>CE</sub> = −10 Vdc, I <sub>0</sub>	tio $_{\text{C}} = -1.0 \text{ mAdc, f} = 1.0 \text{ kHz}$	h <sub>re</sub>	0.1	10	X 10 <sup>-4</sup>
Small – Signal Curren (V <sub>CE</sub> = –10 Vdc, I <sub>0</sub>	t Gain <sub>C</sub> = −1.0 mAdc, f = 1.0 kHz)	h <sub>fe</sub>	100	400	_
Output Admittance (V <sub>CE</sub> = -10 Vdc, I <sub>0</sub>	$_{\rm C} = -1.0 \text{ mAdc, f} = 1.0 \text{ kHz}$	h <sub>oe</sub>	3.0	60	μmhos
Noise Figure (V <sub>CE</sub> = -5.0 Vdc, I	$I_C$ = -100 μAdc, $R_S$ = 1.0 k Ω, f = 1.0 kHz)	NF	_	4.0	dB
SWITCHING CHARA	CTERISTICS				
Delay Time	$(V_{CC} = -3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc})$	t <sub>d</sub>	t <sub>d</sub> – t <sub>r</sub> –		ne
Rise Time	$(I_C = -10 \text{ mAdc}, I_{B1} = -1.0 \text{ mAdc})$	t <sub>r</sub>			ns
Storage Time	$(V_{CC} = -3.0 \text{ Vdc}, I_C = -10 \text{ mAdc})$	t <sub>S</sub>	-	225	ns
Fall Time	$(I_{B1} = I_{B2} = -1.0 \text{ mAdc})$	t <sub>f</sub>	_	75	113

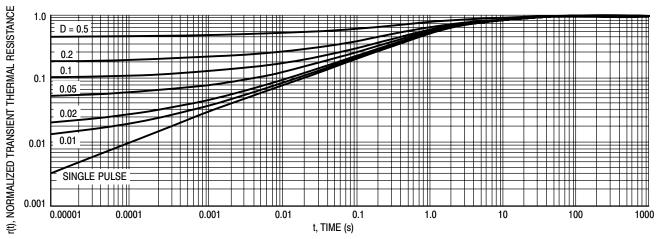
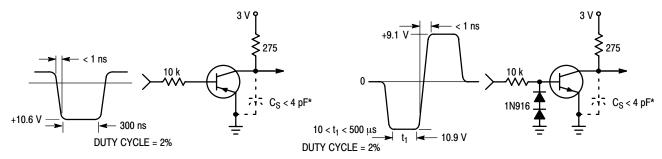


Figure 1. Normalized Thermal Response



\* Total shunt capacitance of test jig and connectors

Figure 2. Delay and Rise Time Equivalent Test Circuit

Figure 3. Storage and Fall Time Equivalent Test Circuit

#### TYPICAL TRANSIENT CHARACTERISTICS

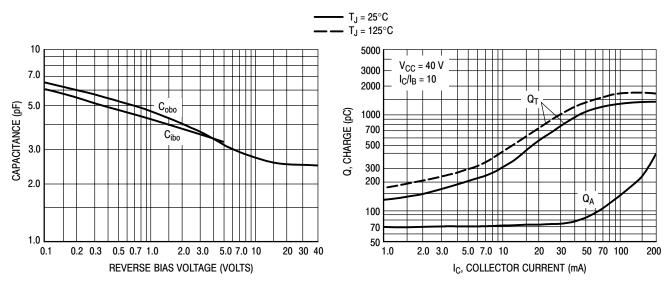
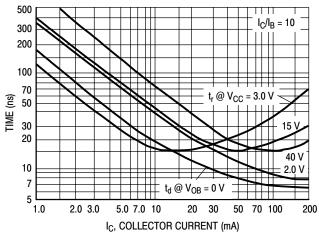


Figure 4. Capacitance

Figure 5. Charge Data



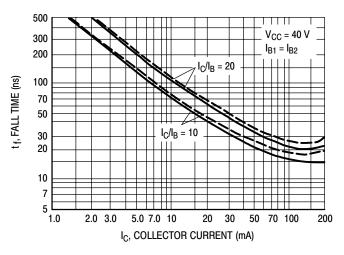
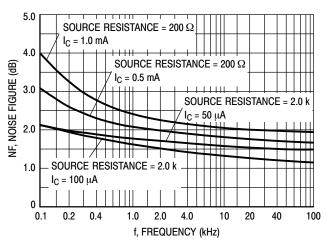


Figure 6. Turn-On Time

Figure 7. Fall Time

## TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

 $(V_{CE} = -5.0 \text{ Vdc}, T_A = 25^{\circ}\text{C}, Bandwidth} = 1.0 \text{ Hz})$ 



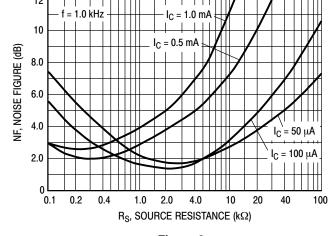
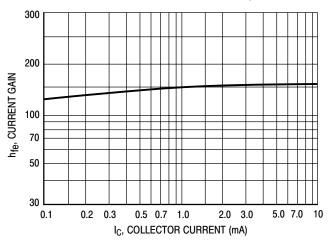


Figure 8.

Figure 9.

#### h PARAMETERS

 $(V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz}, T_A = 25^{\circ}\text{C})$ 



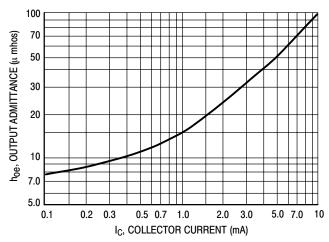
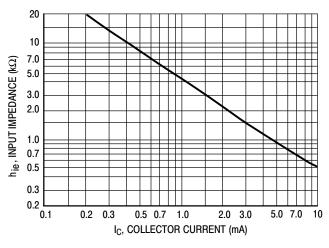


Figure 10. Current Gain

Figure 11. Output Admittance



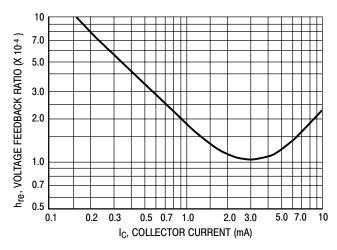


Figure 12. Input Impedance

Figure 13. Voltage Feedback Ratio

#### STATIC CHARACTERISTICS

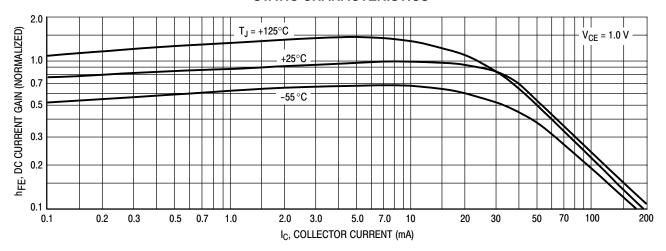


Figure 14. DC Current Gain

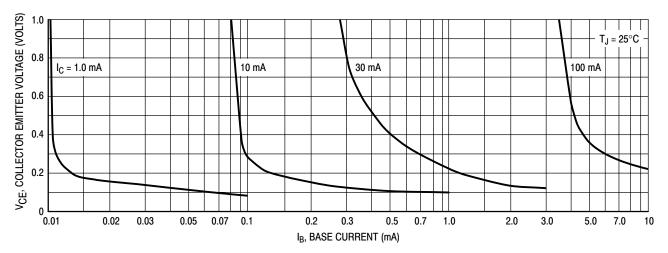
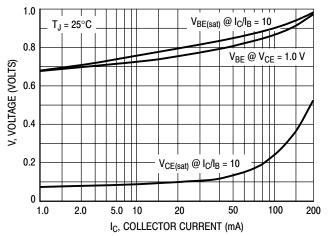


Figure 15. Collector Saturation Region

1.0



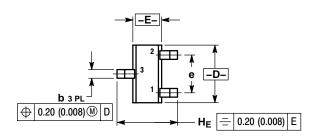
 $\theta_{V_{r}}$  TEMPERATURE COEFFICIENTS (mV/C) 0.5 θ<sub>VC</sub> FOR V<sub>CE(sat)</sub> +25°C TO +125°C -55 °C TO +25°C 0 -0.5 +25°C TO +125°C -1.0  $\theta_{VS}$  FOR  $V_{BE(sat)}$ -55 °C TO +25°C -1.5 0 20 40 100 120 140 180 200 80 160 IC, COLLECTOR CURRENT (mA)

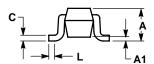
Figure 16. "ON" Voltages

**Figure 17. Temperature Coefficients** 

#### PACKAGE DIMENSIONS

SC-75/SOT-416 CASE 463-01 ISSUE F



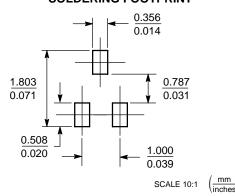


- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
С	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.059	0.063	0.067
Е	0.70	0.80	0.90	0.027	0.031	0.035
е	1.00 BSC			C	0.04 BSC	
L	0.10	0.15	0.20	0.004	0.006	0.008
HE	1.50	1.60	1.70	0.061	0.063	0.065

STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR

#### SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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